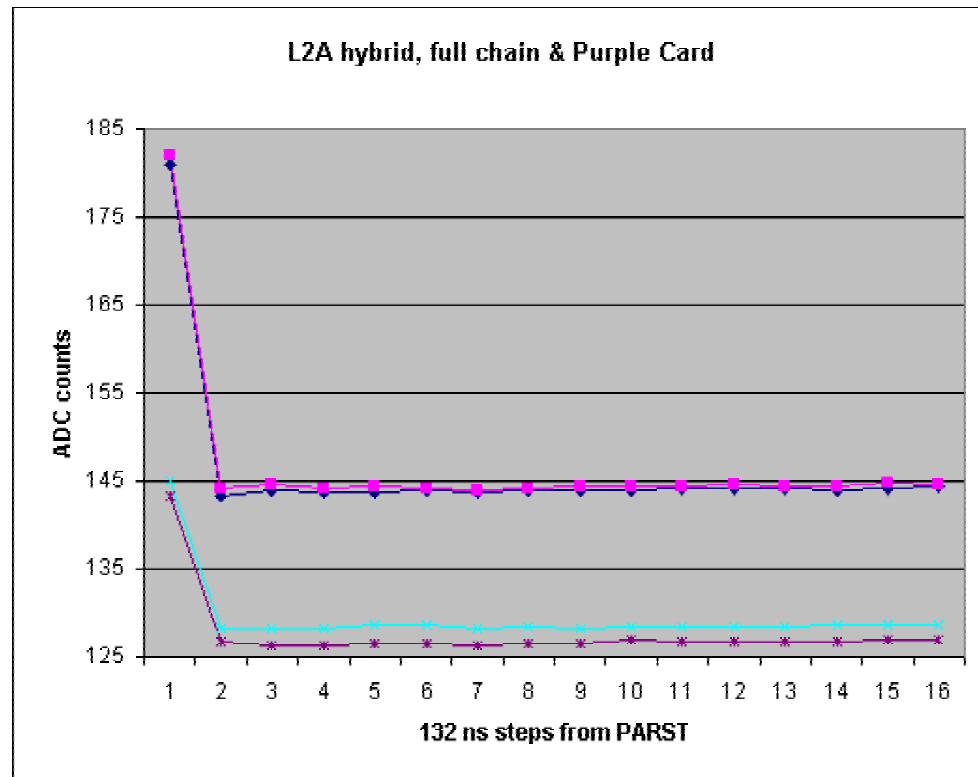




PARST

Andrei Nomerotski 5/12/03

- In SMT see ~ 2 ADC counts oscillations as function of distance to PreamReset (PARST)
 - ♦ May be coming from GND oscillation - reason of concern and recent discussions
- Bare SVX4 measurements does not see this (see Kazu's slides at 2/24/2003 meeting)
- Done measurements addressing this for 10-chip hybrids
 - ♦ With Purple Card (top curves)
 - ♦ With full chain (bottom curves)
 - ♦ No effect seen at 1 ADC count level
 - ♦ Work in progress to check it for L2A2020 module and irradiated module





L0 hybrids

- 50 hybrids did not pass Amitron QA
 - ◆ A few mm crack in the corner
 - ◆ Electrical tests ok
- Received and inspected two hybrids
 - ◆ Crack ~10 micron
 - ◆ Does not cross artwork
 - ◆ 2 day turnaround with ES&H (!)
 - ◆ Dimensions are ok
 - ▲ Flatness 20-40 um (spec 100 um)
 - ▲ Thickness 750 um (spec 800 um)
 - ◆ Will give us 20 hybrids at no charge
 - ◆ Will start a new lot with new delivery date 6/30/2003

